ARM Assembly Language Reference

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| **Comments** | Single line comments:  Multi-line comments: |
| **Load constant into register** | 8 bit immediate:  16 bit immediate:  32 bit immediate (using two instructions):  32 bit immediate (using a pseudoinstruction): |
| **Copy one register into another** | Set r4 to contain the contents of r2 |
| **Load from memory** | Load r0 with the address that’s stored in r1:  Load r0 with an address that’s 4 bytes plus r1: |
| **Store to memory** | Store r0 into the address that’s stored in r1:  Store r0 into the address that’s 8 bytes plus r1: |
| **Add** | Add r0 += 10  Add r0 = r1 + r2 |
| **Subtract** | Subtract r0 -= 1  Subtract r0 = r1 – r2 |
| **Compare** | Update condition flags by comparing r3 with 0 |
| **Conditional branch** |  |

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| **Standard function prologue and epilogue** |  |
| **Calling a function** |  |
| **Allocating memory on the stack** |  |
| **Allocating memory statically** |  |
| **Declaring constants** | Integer constant:  ASCII string: |